

Attorney Docket No.: 00CON159PC-CIP1

In the Claims:

Claim 1 (previously presented): A structure comprising:

- a substrate having a top surface for receiving a die;
- a printed circuit board attached to a bottom surface of said substrate;
- a support pad attached to said top surface of said substrate, said support pad being coupled to a ground bond pad of said die by a down bonding wire, said die being mounted on said support pad;
- at least one via in said substrate;
- said at least one via providing an electrical connection between a signal bond pad of said die and said printed circuit board.

Claim 2 (original): The structure of claim 1 wherein said die is a semiconductor die.

Claim 3 (original): The structure of claim 1 wherein said substrate comprises organic material.

Claim 4 (original): The structure of claim 3 wherein said organic material is selected from the group consisting of polytetrafluoroethylene material and an FR4 based laminate material.

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Claim 5 (original): The structure of claim 1 wherein said substrate comprises a ceramic material.

Claim 6 (original): The structure of claim 1 wherein said at least one via provides an electrical connection between a substrate bond pad and said printed circuit board, wherein said substrate bond pad is electrically connected to said signal bond pad of said die.

Claim 7 (original): The structure of claim 6 wherein said at least one via abuts said substrate bond pad.

Claim 8 (original): The structure of claim 6 wherein said substrate bond pad is electrically connected to said signal bond pad of said die by a signal bonding wire.

Claim 9 (original): The structure of claim 1 wherein said at least one via provides an electrical connection between said signal bond pad of said die and a land, said land being electrically connected to said printed circuit board.

Claim 10 (original): The structure of claim 9 wherein said at least one via abuts said land.

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Claim 11 (original): The structure of claim 1 wherein said at least one via provides an electrical connection between a substrate bond pad and a land, wherein said substrate bond pad is electrically connected to said signal bond pad of said die, and wherein said land is electrically connected to said printed circuit board.

Claim 12 (original): The structure of claim 11 wherein said at least one via abuts said substrate bond pad and said land.

Claim 13 (original): The structure of claim 11 wherein said substrate bond pad is electrically connected to said signal bond pad of said die by a signal bonding wire.

Claim 14 (original): The structure of claim 12 wherein said substrate bond pad is electrically connected to said signal bond pad of said die pad by a signal bonding wire.

Claim 15 (original): The structure of claim 1 wherein said at least one via comprises copper.

Claim 16 (original): The structure of claim 1 wherein said at least one via comprises a thermally conductive material.

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Claims 17-71 (canceled).

Claim 72 (new): A structure comprising:

a substrate having a top surface and a bottom surface;
a semiconductor die attached to said top surface of said substrate;
a heat spreader attached to said bottom surface of said substrate;
a support pad attached to said top surface of said substrate, said support pad being connected to said heat spreader;
a first via in said substrate;
said first via providing a connection between said semiconductor die and said heat spreader, wherein said heat spreader is an electrical conductor, wherein said structure further comprises a substrate down bond area attached to said top surface of said substrate, and wherein said first via provides an electrical connection between said substrate down bond area and said heat spreader.

Claim 73 (new): The structure of claim 72 wherein a semiconductor die ground bond pad on said semiconductor die is electrically connected to said substrate down bond area by a down bonding wire.

Claim 74 (new): A structure comprising:

a substrate having a top surface and a bottom surface;

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a semiconductor die attached to said top surface of said substrate;
a heat spreader attached to said bottom surface of said substrate;
a support pad attached to said top surface of said substrate, said support pad being
connected to said heat spreader;
a first via in said substrate;
said first via providing a connection between said semiconductor die and said heat
spreader, said first via further providing an electrical connection between said
semiconductor die and said heat spreader.